

<b>Notice of Allowability</b>	Application No.	Applicant(s)
	09/773,870	TANAKA ET AL.
	Examiner VIJAY SHANKAR	Art Unit 2629

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address--

All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. **THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS.** This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.

1.  This communication is responsive to Amendment 3/19/07 and Oath 6/14/07.

2.  The allowed claim(s) is/are 1-27.

3.  Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).

a)  All    b)  Some\*    c)  None    of the:

1.  Certified copies of the priority documents have been received.

2.  Certified copies of the priority documents have been received in Application No. 08/835,207.

3.  Copies of the certified copies of the priority documents have been received in this national stage application from the International Bureau (PCT Rule 17.2(a)).

\* Certified copies not received: \_\_\_\_\_

Applicant has THREE MONTHS FROM THE "MAILING DATE" of this communication to file a reply complying with the requirements noted below. Failure to timely comply will result in ABANDONMENT of this application.

**THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.**

4.  A SUBSTITUTE OATH OR DECLARATION must be submitted. Note the attached EXAMINER'S AMENDMENT or NOTICE OF INFORMAL PATENT APPLICATION (PTO-152) which gives reason(s) why the oath or declaration is deficient.

5.  CORRECTED DRAWINGS ( as "replacement sheets") must be submitted.

(a)  including changes required by the Notice of Draftsperson's Patent Drawing Review ( PTO-948) attached  
1)  hereto or 2)  to Paper No./Mail Date \_\_\_\_\_.

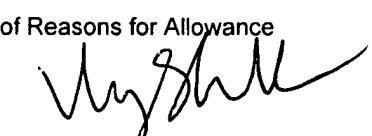
(b)  including changes required by the attached Examiner's Amendment / Comment or in the Office action of  
Paper No./Mail Date \_\_\_\_\_.

Identifying indicia such as the application number (see 37 CFR 1.84(c)) should be written on the drawings in the front (not the back) of each sheet. Replacement sheet(s) should be labeled as such in the header according to 37 CFR 1.121(d).

6.  DEPOSIT OF and/or INFORMATION about the deposit of BIOLOGICAL MATERIAL must be submitted. Note the attached Examiner's comment regarding REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL MATERIAL.

#### Attachment(s)

1.  Notice of References Cited (PTO-892)
2.  Notice of Draftsperson's Patent Drawing Review (PTO-948)
3.  Information Disclosure Statements (PTO/SB/08),  
Paper No./Mail Date \_\_\_\_\_
4.  Examiner's Comment Regarding Requirement for Deposit  
of Biological Material
5.  Notice of Informal Patent Application
6.  Interview Summary (PTO-413),  
Paper No./Mail Date \_\_\_\_\_
7.  Examiner's Amendment/Comment
8.  Examiner's Statement of Reasons for Allowance
9.  Other \_\_\_\_\_



VIJAY SHANKAR  
Primary Examiner  
Art Unit: 2629

***Reissue Applications***

**EXAMINER'S AMENDMENT**

1. An examiner's amendment to the record appears below. Should the changes and/or additions be unacceptable to applicant, an amendment may be filed as provided by 37 CFR 1.312. To ensure consideration of such an amendment, it MUST be submitted no later than the payment of the issue fee.

Authorization for this examiner's amendment was given in a telephone interview with H. Warren Burnam, Jr. on 9/19/08.

The application has been amended as follows:

- 1) In Amendment filed on 3/13/07, Claim 18, line 1 was not underlined.

Claim 18. (New) A pair of substrates sandwiching liquid crystals therebetween;  
a plurality of scanning lines to which scanning signals are successively applied;  
a plurality of signal lines to which data signals are successively applied, said signal lines  
intersecting said scanning lines at right angles;  
a switching element which is arranged in a vicinity of each of intersections of said  
scanning lines and said signal lines, and electrically connected to both of said scanning  
and signal lines;  
a pixel electrode connected to each of said switching elements;  
said scanning lines, signal lines, switching elements and pixel electrodes being formed  
on one of said substrates,  
a common electrode formed on the other of said substrates so that said common

electrode faces said pixel electrode with said liquid crystals therebetween; a common line to which a common signal is applied;  
a pixel capacitance, one of electrodes of said pixel capacitance being connected to said common line; and  
a dummy scanning line formed outside of one of said scanning lines located at an outermost position on either a scanning start side or a scanning end side of scanning signal, for producing a parasitic capacitance between said dummy scanning line and the pixel electrode connected to the scanning line located at the outermost position.

***Priority***

1. Acknowledgment is made of applicant's claim for foreign priority under 35 U.S.C. 119(a)-(d). The certified copy has been filed in parent Application No. 08/835,207, filed on 04/07/1997.

***Allowable Subject Matter***

2. Claims 1-27 are allowed.

3. The following is an examiner's statement of reasons for allowance: The prior arts Aoki et al, Ogawa et al, den Boer et al, Yao et al fails to teach a pair of substrates sandwiching liquid crystals comprising a pixel electrode connected to each of the switching elements; the scanning lines, signal lines, switching elements and pixel electrodes being formed on one of the substrates, a common electrode formed on the other of the substrates so that the common electrode faces the pixel electrode with the

liquid crystals therebetween; a common line to which a common signal is applied; a pixel capacitance, one of electrodes of the pixel capacitance being connected to the common line; and a dummy scanning line formed outside of one of the scanning lines located at an outermost position on either a scanning start side or a scanning end side of scanning signal, for producing a parasitic capacitance between the dummy scanning line and the pixel electrode connected to the scanning line located at the outermost position as claimed by Claim 18.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

4. Any inquiry concerning this communication or earlier communications from the examiner should be directed to VIJAY SHANKAR whose telephone number is (571) 272-7682. The examiner can normally be reached on M-F 7:30 am - 5:30 pm.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, MICHAEL RAZAVI can be reached on (571) 272-7664. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

/VIJAY SHANKAR/  
Primary Examiner, Art Unit 2629

VS